

## ***SHORT CURRICULUM VITAE***



### **Studies:**

- **1993-1997: PhD in Computer Science** ("*Contributions to Instruction Level Parallel Architectures*"), **"Politehnica" University of Timisoara**, Romania, Supervisor: **Professor Crisan STRUGARU, PhD** - a pioneer of Microprocessors and Microsystems, Honorary Member of the **"Technical Sciences Academy of Romania"**. Prof. Strugaru received the prestigious "Traian Vuia" Romanian Academy Award for Technical Sciences in 1983. **Here, some of my thoughts about Professor Strugaru**
- **1982-1987: Faculty of Computer Engineering and Control Systems**, **"Politehnica" University of Timisoara**, Romania (Master Degree, honors)
- (1981-1982: Obligatory Military Service; Officer Degree)
- **1977-1981: "Gheorghe Lazar" High School of Mathematics and Physics** from Sibiu, Romania

### **Professional Experience:**

- **2001-2012 & 2015-present: I was the first PhD Supervisor** in *Computer Science / Computer Engineering and Information Technology* (see here!) domain at **"Lucian Blaga" University of Sibiu**. I supervised **the first 5 doctors in Computer Science domain** graduating at "Lucian Blaga" University of Sibiu!
- **2012-2015: PhD Supervisor** in *Computer Engineering and Information Technology* domain at **Technical University of Cluj-Napoca** (due to the national legislative context)
- **2000-present: Professor, Department of Computer Science & Electrical Engineering**, Engineering School, **"Lucian Blaga" University of Sibiu** (Courses: Computer Architecture, Advanced Computer Architecture, Research Methods in Advanced Computing Architectures); I was also a **Visiting Research Fellow** at **University of Hertfordshire, United Kingdom**.
- **1991-2000: Lecturer / Senior Lecturer/ Associate Professor**, Department of Computer Science & Engineering, "Lucian Blaga" University of Sibiu
- **1987-1991: Minisystems Testing/Diagnosing/Debugging Engineer** at I.I.R.U.C. Bucharest / Sibiu Branch (I was specialised on the Romanian I-102F/4M computing system, compatible with DEC PDP-11/44 minisystem.)

**Lucian N. VINTAN** is currently Professor and Ph.D. Supervisor in Computer Engineering at “Lucian Blaga” University of Sibiu, Romania. He led the *Advanced Computer Architecture and Processing Systems Research Centre* from this university (see <http://acaps.ulbsibiu.ro/>). He obtained the MSc degree in Computer Engineering from "Politehnica" University of Timisoara (1987), Romania, and the PhD title in Computer Engineering from the same university (1997).

Professor Lucian VINTAN is an expert in the areas of instruction/thread level parallelism, multi-core and many-core systems, automatic design space exploration (multi-objective optimization and meta-optimization), prediction techniques in ubiquitous computing systems and text mining (classification/clustering). He published over 160 scientific papers in some prestigious journals like *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, *Information Sciences* (Elsevier), *Concurrency and Computation: Practice and Experience* (John Wiley & Sons), *Journal of Systems Architecture* (Elsevier), *IET Computers & Digital Techniques* (United Kingdom), *Microprocessors and Microsystems* (Elsevier) etc., and international conferences (IEEE, ACM) from Romania, USA, UK, Portugal, Italy, Austria, Czech Republic, Hungary, Poland, Germany, China, Slovenia, Latvia, Spain, Turkey, etc. He introduced some well-known ideas, especially in Computer Architecture domain (Dynamic Neural Branch Prediction – over 100 citations up to this moment, Pre-Computed Branches, Unbiased Hard-Predictable Branches, Dynamic Value Prediction focused on CPU's Context, Selective Dynamic Value Prediction/Dynamic Instruction Reuse, Multi-Objective Optimization augmented with Computer Architecture Domain-Knowledge represented using Fuzzy Logic Rules, Meta-Optimization, etc.) His publications acquired over 850 citations through papers published in international conferences and scientific journals (example *IEEE Transactions on Computers*); His Hirsch index is H-index=17 (see <http://scholar.google.com/citations?user=9NiMZo4AAAAJ&hl=ro>, accessed at February 17<sup>th</sup> 2019.)

He was the first researcher proposing the idea of *Dynamic Neural Branch Prediction* (IJCNN '99, Washington DC), pioneering the application of some Machine Learning techniques to Computing Systems research. In that paper he wrote: „*At this time, our intuition is that a simplified neural predictor could be designed within the timing restraints of a superscalar processor. [...] the cost would be far less than one of Two Level Adaptive predictors and it may even be possible to implement multiple cut-down neural predictors, associated which each branch.*” The idea was implemented in some advanced commercial microprocessors like Oracle SPARC T4-4 (2011) – <http://www.oracle.com/us/products/servers-storage/servers/sparc-enterprise/t-series/sparc-t4-4-faq-496527.pdf>, AMD Bulldozer (2011), AMD Bobcat/Jaguar (2014) – <http://www.realworldtech.com/jaguar/2/>, Samsung Exynos M1 Processor (quadcore, ISA ARM v8.0, 64/32 bits, 2016) – <https://weekly-geekly.github.io/articles/397075/index.html> (citation L. Vințan), <http://www.microsofttranslator.com/bv.aspx?from=&to=en&a=http%3A%2F%2Fpc.watc.h.impress.co.jp%2Fdocs%2Fcolumn%2Fkaigai%2F1036983.html>, AMD Ryzen (8 cores, 16-thread chip, 2017) – <http://www.amd.com/en-us/press-releases/Pages/amd-takes-computing-2016dec13.aspx>, IBM z14 (2017), etc. After this paper he firstly introduced the perceptron branch predictor concept – please see his paper available online at [http://webspace.ulbsibiu.ro/lucian.vintan/html/Rom\\_JIST.pdf](http://webspace.ulbsibiu.ro/lucian.vintan/html/Rom_JIST.pdf)).

▪ „*Congratulations! You deserve this recognition.*” – Prof. Mateo Valeo (IEEE/ACM Eckert-Mauchly Award), UPC Barcelona & BSC

- „You can be proud that you have contributed to efforts that had real impact.” – **Prof. Douglas Comer**, Purdue University, USA
- “Congrats with this great achievement!” – **Prof. Rainer Leupers**, RWTH Aachen University
- “I’d like to congratulate you for the significant industrial application of your proposed solution.” - **Prof. Cristina Silvano** (IEEE Fellow), Politecnico di Milano
- „My congratulation for your impressive scientific achievements, first of all for being the first proposing the use of dynamic neural branch prediction that became finally commercially employed in a number of processors.” – **Prof. Dezső Sima**, Obuda University

**He pioneered not only using Machine Learning methods in Computing Systems research but also the application of some techniques belonging to domains like Decision Systems, Fuzzy Logics, Information Theory, Optimization, Petri Nets, etc. in Computer Architecture research** (see especially the papers B11, B24, B25, B28, B35, B39, B48, B57, B65, B69, B70, B75, B77, B80, B81, B83, B94, B101, B102, B105, etc. from the list online available at <http://webspace.ulbsibiu.ro/lucian.vintan/html/Lista.doc>).



Furthermore, he has authored 6 books on the above subjects. Two of them, written in English, are used as text-books on the subject at some leading universities around the world. He finalized as a Project Manager 10 national and 2 European research grants obtained through competition. Until 2018, 8 PhD students graduated under his supervision.

In the year 2002 Professor VINTAN obtained a Visiting Research Fellow title from the University of Hertfordshire (UH), UK, as recognition of “*your ongoing collaboration with our research activities.*” (Prof. J. M. Senior, PhD, Dean of the Faculty of Engineering and Information Sciences, UH.) Professor VINTAN has received various awards and recognitions for his scientific merits. He received **The Romanian Academy “Tudor Tanasescu” Prize in 2005**. In 2012 he was elected **full-member of The Technical Sciences Academy of Romania** (correspondent member in 2005). Starting from 2005, as a **European Commission DG Information Society Expert** he is actively involved in EU-funded projects from the evaluation of proposals to the managing and reviewing of many projects. From October 2012 he was accepted as a HiPEAC member – see <https://www.hipeac.net/~lucian.vintan/>. He has served on the technical program committee of over 170 international computer systems conferences and has been peer-reviewed hundreds of research papers for numerous international journals and conferences.

**Fields of Interest:** Computer Architecture, Multi-objective Optimization Methods, Text Mining

Details about his professional activity, including a List of Publications, might be found at URL: <http://webspace.ulbsibiu.ro/lucian.vintan/html/> (in English) and <http://www.astr.ro/prof-univ-dr-ing-lucian-nicolae-vintan/> (in Romanian).

September 9<sup>th</sup> 2018, Sibiu

**Professor Lucian N. VINTAN, PhD**  
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<http://webspace.ulbsibiu.ro/lucian.vintan/html/> ( EN)  
<http://www.astr.ro/prof-univ-dr-ing-lucian-nicolae-vintan/> ( RO)

**ANNEX. SELECTED PUBLISHED PAPERS** (the complete list is online available at <http://webspace.ulbsibiu.ro/lucian.vintan/html/Lista.doc>)

**A. PAPERS PUBLISHED IN (ISI) THOMSON REUTERS JOURNALS WoS (SELECTION)**

1. **VINTAN L.** – *Towards a Powerful Dynamic Branch Predictor*, Romanian Journal of Information Science and Technology, vol. 3, nr. 3, pg. 287-301, ISSN: 1453-8245, **Romanian Academy**, Bucharest, 2000. **The first paper that introduces the perceptron branch predictor!**
2. EGAN C., STEVEN G., **VINTAN L.** – *Cached Two-Level Adaptive Branch Predictors with Multiple Stages*, pp. 179-191, **Lecture Notes in Computer Science**, vol. 2299, **Springer-Verlag**, ISSN 0302-9743, ISBN 3-540-43409-7, **Berlin Heidelberg**, 2002
3. EGAN C., STEVEN G., QUICK P., ANGUERA R., **VINTAN L.** – *Two-Level Branch Prediction using Neural Networks*, **Journal of Systems Architecture**, vol. 49, issues 12-15, pp. 557-570, ISSN: 1383-7621, **Elsevier**, December 2003
4. **VINTAN L.** – *Value Prediction and Speculation into the Next Microprocessors Generation*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 5, Number 3, pp. 321-328, ISSN 1454-9069, Bucharest, 2004
5. **VINTAN L.**, FLOREA A., GELLERT A. – *Focalising Dynamic Value Prediction to CPU's Context*, **Computers & Digital Techniques, IEE Proceedings** (from 2006: **IET Computers and Digital Techniques**, ISSN: 1751-8601), **United Kingdom**, Vol. 152, No. 4, ISSN 1350-2387, 2005
6. **L. VINTAN**, A. GELLERT, A. FLOREA, M. OANCEA, C. EGAN – *Understanding Prediction Limits through Unbiased Branches*, **Lecture Notes in Computer Science. Advances in Computer Systems Architecture**, vol. 4186, pp. 480-487, ISSN 0302-9743, ISBN-13 978-3-540-40056, **Springer-Verlag Berlin / Heidelberg**, 2006
7. GELLERT A., **VINTAN L.** – *Person Movement Prediction using Hidden Markov Models*, Studies in Informatics and Control, Vol. 15, No. 1, pp. 17-30, ISSN: 1220-1766, National Institute for Research and Development in Informatics, Bucharest, March 2006
8. **VINTAN L. N.**, FLOREA A., GELLERT A. – *Random Degrees of Unbiased Branches*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 9, Number 3, pp. 259 - 268, ISSN 1454-9069, Bucharest, 2008
9. GELLÉRT Á., FLOREA A., **VINTAN L.**, - *Exploiting Selective Instruction Reuse and Value Prediction in a Superscalar Architecture*, **Journal of Systems Architecture**, vol. 55, issues 3, pp. 188-195, ISSN 1383-7621, **Elsevier**, 2009

10. GELLÉRT Á., CALBOREAN H., VINȚAN L., FLOREA A. - *Multi-Objective Optimisations for a Superscalar Architecture with Selective Value Prediction*, **IET Computers & Digital Techniques, United Kingdom**, Vol. 6, Issue 4, pp. 205-213, ISSN: 1751-8601, 2012
11. C. RADU, MD. S. MAHBUB, L. VINTAN - *Developing Domain-Knowledge Evolutionary Algorithms for Network-on-Chip Application Mapping*, **Microprocessors and Microsystems**, vol. 37, issue 1, pp. 65-78, ISSN: 0141-9331, **Elsevier**, February 2013
12. JAHR R., CALBOREAN H., VINȚAN L., UNGERER T. - *Finding Near-Perfect Parameters for Hardware and Code Optimizations with Automatic Multi-Objective Design Space Explorations*, **Concurrency and Computation: Practice and Experience**, doi: 10.1002/cpe.2975, Volume 27, Issue 9, pp. 2196-2214, Print ISSN 1532-0626, Online ISSN: 1532-0634, **John Wiley & Sons**, 2015 (**Q2/TR WoS**)
13. VINTAN L., CHIS R., MD. ALI ISMAIL, COTOFANA C. – *Improving Computing Systems Automatic Multi-Objective Optimization through Meta-Optimization*, **IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems**, ISSN: 0278-0070, DOI 10.1109/TCAD.2015.2501299, vol. 35, issue 7, 2016 (**Q1/TR WoS**)
14. CHIȘ R., FLOREA A., BUDULECI C., VINȚAN L. – *Multi-Objective Optimization for an Enhanced Multi-Core SNIPER Simulator*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 19, Number 1, pp. 85-93, ISSN 1454-9069, Bucharest, January-March 2018 (**Q2 WoS cf. JCR 2016**)
- Á. GELLÉRT, A. FLOREA, U. FIORE, P. ZANETTI, L. VINȚAN – *Performance and Energy Optimisation in CPUs through Fuzzy Knowledge Representation*, **Information Sciences**, Volume 476, ISSN: 0020-0255, DOI: 10.1016/j.ins.2018.03.029, **Elsevier**, 2019, v. [http://webpace.ulbsibiu.ro/lucian.vintan/html/IS\\_2018.pdf](http://webpace.ulbsibiu.ro/lucian.vintan/html/IS_2018.pdf) Revista a fost încadrată în “**zona roșie**” **Q1 WoS** în domeniul *Computer Science* (**12/148 IF**, **29/148 AIS**), cf. JCR 2017 (publicat în iunie 2018)
15. GELLÉRT Á., VINȚAN L. – *A Multicore Architecture with Selective Load Value Prediction*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 19, Number 4, ISSN 1454-9069, Bucharest, 2018. Online available at [http://webpace.ulbsibiu.ro/lucian.vintan/html/Proc\\_Rom\\_Acad\\_2018.pdf](http://webpace.ulbsibiu.ro/lucian.vintan/html/Proc_Rom_Acad_2018.pdf). **Revista este cotată Clarivate Analytics Web of Science**, **IF = 1,752**, AIS=0,248, cf. JCR 2017 (publicat în iunie 2018), **Q2 WoS cf. JCR 2017**

## B. PAPERS PUBLISHED IN INTERNATIONAL CONFERENCES (SELECTION)

1. STEVEN G. B., VINȚAN L. - *Modelling Superscalar Pipelines with Finite State Machines*, "Proceedings of the **22<sup>nd</sup> Euromicro'96 Conference**. Beyond 2000: Hardware/Software Design Strategies", September 1996, Prague, Czech Republic, pp. 20-25, IEEE Computer Society Press, Los Alamitos, California, USA, ISBN 0-8186-7703-1, **Library of Congress Number 96-79894**
2. VINTAN L., STEVEN G. B. - *Memory Hierarchy Limitations in Multiple Instruction-Issue Processor Design*, "Proceedings **23-rd Euromicro Conference**. New Frontiers of



Information Technology", September 1997, Budapest, Hungary, pp. 252-257, IEEE Computer Society Press, Los Alamitos, California, **USA**, 1997, ISBN 0-8186-8215-9, **Library of Congress Number 97-81043**.

3. **VINTAN L.**, ARMAT C., STEVEN G. - *The Impact of Cache Organisation on the Instruction Issue Rate of a Superscalar Processor*, **Proceedings of Euromicro 7-th Workshop on Parallel and Distributed Systems**, pg. 58-65, ISBN 0-7695-0059-5, Funchal, **Portugal**, 3<sup>rd</sup> – 5<sup>th</sup> February, IEEE Computer Society Press, 1999

4. **VINTAN L.** - *Towards a High Performance Neural Branch Predictor*, Proceedings of The International Joint Conference on Neural Networks - IJCNN '99 (CD-ROM, ISBN 0-7803-5532-6; Abstract in IJCNN'99 Book of Summaries, art. 2106), pp. 868 – 873, vol. 2, **Washington DC, USA**, 10-16 July, IEEE, 1999, Digital Object Identifier: [10.1109/IJCNN.1999.831066](https://doi.org/10.1109/IJCNN.1999.831066). **Computer Science Conference Rank** - <http://lipn.univ-paris13.fr/~bennani/CSRank.html>. **The first paper that introduces the neural branch predictor!**

5. **VINTAN L.**, EGAN C. - *Extending Correlation in Branch Prediction Schemes*, Proceedings of **25<sup>th</sup> Euromicro International Conference**, Milano, **Italy**, 8-10 September, IEEE Computer Society Press, ISBN 0-7695-0321-7, 1999)

6. STEVEN G., EGAN C., ANGUERA R., **VINTAN L.** – *Dynamic Branch Prediction using Neural Networks*, Proceedings of **International Euromicro Conference DSD '2001**, IEEE Computer Society Press, ISBN 0-7695-1239-9, Warsaw, **Poland**, September, 2001 (pp.178-185), Digital Object Identifier: [10.1109/DSD.2001.952279](https://doi.org/10.1109/DSD.2001.952279)

7. STEVEN G., EGAN C., SHIM W., **VINTAN L.** – *Applying Caching to Two-Level Adaptive Branch Prediction*, Proceedings of **International Euromicro Conference DSD '2001**, IEEE Computer Society Press, ISBN 0-7695-1239-9, Warsaw, **Poland**, September, 2001 (pg.186-193)

8. PETZOLD J., BAGCI F., TRUMLER W., UNGERER T., **VINTAN L.** – *Global State Context Prediction Techniques Applied to a Smart Office Building*, 2004 **Communication Networks and Distributed Systems Modelling and Simulation Conference** (CNDS '04), San Diego, California, **USA**, January 18-21, 2004

9. **VINTAN L.**, GELLERT A., UNGERER T., PETZOLD J. – *Person Movement Prediction Using Neural Networks*, KI 2004 Proceedings Workshop on Modelling and Retrieval of Context, University of Ulm, **Germany**, ISSN 1613-0073, September 20<sup>th</sup> -21<sup>st</sup> 2004

10. GELLERT A., A. FLOREA, M. VINTAN, C. EGAN, **L. VINTAN** - *Unbiased Branches: An Open Problem*, **The Twelfth Asia-Pacific Computer Systems Architecture Conference** (ACSAC 2007), Seoul, **Korea**, August 23<sup>rd</sup>-25<sup>th</sup>, 2007; **Lecture Notes in Computer Science**. [Advances in Computer Systems Architecture](https://doi.org/10.1007/978-3-540-74308-8_2), vol. 4697, pp. 16-27, ISSN 0302-9743 (Print) 1611-3349 (Online), ISBN 978-3-540-74308-8, **Springer-Verlag Berlin / Heidelberg**, 2007

11. GELLERT A., PALERMO G., ZACCARIA V., FLOREA A., **VINTAN L.**, SILVANO C. - *Energy-Performance Design Space Exploration in SMT Architectures Exploiting Selective Load Value Predictions*, **Design, Automation & Test in Europe International Conference**

**(DATE 2010)**, March 8-12, 2010, Dresden, **Germany**, ISBN 978-3-9810801-6-2, pp. 271-274 (<http://www.date-conference.com/front>; 326 accepted papers from over 980 submitted papers!)

12. CRETULESCU R., MORARIU D., VINTAN L., COMAN I. D. – *An Adaptive Meta-classifier for Text Documents*, The **16<sup>th</sup> International Conference on Information Systems Analysis and Synthesis** (ISAS 2010), vol. 2, pp. 372-377, ISBN-13: 978-1-934272-88-6, **Orlando Florida, USA**, April 6<sup>th</sup> – 9<sup>th</sup> 2010

13. R. JAHR, T. UNGERER, H. CALBOREAN, L. VINTAN - *Automatic Multi-Objective Optimization of Parameters for Hardware and Code Optimizations*, Proceedings of the 2011 **International Conference on High Performance Computing & Simulation** (HPCS 2011), pp. 308-316, Publisher: **IEEE**, ISBN 978-1-61284-381-0, **Istanbul, Turkey**, July 2011

14. JAHR R., CALBOREAN H., VINTAN L., UNGERER T. - *Boosting Design Space Explorations with Existing or Automatically Learned Knowledge*, The **16-th International GI/ITG Conference on Measurement, Modelling and Evaluation of Computing Systems and Dependability and Fault Tolerance** (MMB/DFT 2012), March 19-21, 2012, **Kaiserslautern, Germany**; [Lecture Notes in Computer Science](#), 2012, Volume 7201/2012, pp. 221-235, **Springer-Verlag Berlin Heidelberg**, ISSN 0302-9743, ISBN 978-3-642-28539-4, DOI: 10.1007/978-3-642-28540-0\_16 – v. <http://www.mmb2012.de/>; <http://www.springerlink.com/content/978-3-642-28539-4#section=1045830&page=1&locus=0>

Some of my most relevant publications in:

- developing **CPU automatic multi-objective optimization methods (1)** and
- developing **low power CPU architectures based on predictive and speculative techniques (2)**.

Below you can have a look on my most important research papers related to these topics (**all the papers are available online through the corresponding links**).

- VINTAN L., FLOREA A., GELLERT A. – *Focalising Dynamic Value Prediction to CPU's Context*, **Computers & Digital Techniques**, **IEE Proceedings** (from 2006: **IET Computers and Digital Techniques**, ISSN: 1751-8601), **United Kingdom**, Vol. 152, No. 4, ISSN 1350-2387, July 2005, v. <http://webpace.ulbsibiu.ro/lucian.vintan/html/IEE.pdf>
- GELLERT, A. FLOREA, L. VINTAN - *Exploiting Selective Instruction Reuse and Value Prediction in a Superscalar Architecture*, **Journal of Systems Architecture**, vol. 55, issues 3, pp. 188-195, ISSN 1383-7621, **Elsevier**, 2009, v. <http://webpace.ulbsibiu.ro/lucian.vintan/html/jsa2009.pdf>
- GELLERT A., PALERMO G., ZACCARIA V., FLOREA A., VINTAN L., SILVANO C. – *Energy-Performance Design Space Exploration in SMT Architectures Exploiting Selective Load Value Predictions*, **Design, Automation & Test in Europe International Conference (DATE 2010)**, March 8-12, 2010, Dresden, Germany, ISBN 978-3-9810801-6-2, pp. 271-274, v. [http://webpace.ulbsibiu.ro/lucian.vintan/html/Date\\_2010.pdf](http://webpace.ulbsibiu.ro/lucian.vintan/html/Date_2010.pdf)
- Á. GELLÉRT, H. CALBOREAN, L. VINTAN, A. FLOREA – *Multi-Objective Optimizations for a Superscalar Architecture with Selective Value Prediction*, **IET Computers & Digital Techniques**, **United Kingdom**, Vol. 6, Issue 4, pp. 205-213, ISSN: 1751-8601, 2012, v. <http://webpace.ulbsibiu.ro/lucian.vintan/html/CDT2012.pdf>
- JAHR R., CALBOREAN H., VINTAN L., UNGERER T. – *Finding Near-Perfect Parameters for Hardware and Code Optimizations with Automatic Multi-Objective Design*

*Space Explorations, Concurrency and Computation: Practice and Experience*, doi: 10.1002/cpe.2975, Volume 27, Issue 9 (June 25), pp. 2196-2214, Print ISSN 1532-0626, Online ISSN: 1532-0634, John Wiley & Sons, 2015, v. <http://webspaces.ulbsibiu.ro/lucian.vintan/html/CCPE.pdf>

- RADU, MD. S. MAHBUB, L. VINȚAN – *Developing Domain-Knowledge Evolutionary Algorithms for Network-on-Chip Application Mapping*, **Microprocessors and Microsystems**, vol. 37, issue 1, pp. 65-78, ISSN: 0141-9331, Elsevier, February 2013. V. [http://webspaces.ulbsibiu.ro/lucian.vintan/html/Micro\\_2013.pdf](http://webspaces.ulbsibiu.ro/lucian.vintan/html/Micro_2013.pdf)

- VINȚAN L., CHIȘ R., MD. ALI ISMAIL, COȚOFANĂ C. – *Improving Computing Systems Automatic Multi-Objective Optimization through Meta-Optimization*, **IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems**, ISSN: 0278-0070, Volume 35, Issue 7, pp. 1125-1129, (DOI 10.1109/TCAD.2015.2501299), July 2016, v. <http://webspaces.ulbsibiu.ro/lucian.vintan/html/TCAD.pdf> Această revistă a fost încadrată în “**zona roșie**” **Q1 TR WoS** cf. JCR 2014, publicat în iunie 2015 și cf. UEFISCDI – v. [http://uefiscdi.gov.ro/userfiles/file/PREMIERE\\_ARTICOLE/ARTICOLE%202014/IF\\_2013.pdf](http://uefiscdi.gov.ro/userfiles/file/PREMIERE_ARTICOLE/ARTICOLE%202014/IF_2013.pdf) (pg. 70/486, Computer Science, Hardware & Architecture). **ESI Total Citations / Engineering (2012-2016) Q1 TR WoS**

- CHIȘ R., FLOREA A., BUDULECI C., VINȚAN L. – *Multi-Objective Optimization for an Enhanced Multi-Core SNIPER Simulator*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 19, Number 1, pp. 85-93, ISSN 1454-9069, Bucharest, January-March 2018, v. <http://www.acad.ro/sectii2002/proceedings/doc2018-1/12.pdf>

- CHIȘ R., VINȚAN L. – *Developing Automatic Multi-Objective Optimization Methods for Complex Actuators*, *Advances in Electrical and Computer Engineering*, Vol. 17, Issue 4, pp. 89-98, ISSN: 1582-7445, November 2017, see <http://webspaces.ulbsibiu.ro/lucian.vintan/html/aece.pdf>.

- Á. GELLÉRT, A. FLOREA, U. FIORE, P. ZANETTI, L. VINȚAN – *Performance and Energy Optimisation in CPUs through Fuzzy Knowledge Representation*, **Information Sciences**, Volume 476, ISSN: 0020-0255, DOI: 10.1016/j.ins.2018.03.029, Elsevier, 2019, v. [http://webspaces.ulbsibiu.ro/lucian.vintan/html/IS\\_2018.pdf](http://webspaces.ulbsibiu.ro/lucian.vintan/html/IS_2018.pdf) Revista a fost încadrată în “**zona roșie**” **Q1 WoS** în domeniul *Computer Science* (**12/148 IF**, **29/148 AIS**), cf. JCR 2017 (publicat în iunie 2018)

- GELLÉRT Á., VINȚAN L. – *A Multicore Architecture with Selective Load Value Prediction*, **Proceedings of The Romanian Academy**, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 19, Number 4, ISSN 1454-9069, Bucharest, 2018. Disponibil online la [http://webspaces.ulbsibiu.ro/lucian.vintan/html/Proc\\_Rom\\_Acad\\_2018.pdf](http://webspaces.ulbsibiu.ro/lucian.vintan/html/Proc_Rom_Acad_2018.pdf). **Revista este cotată Clarivate Analytics Web of Science, IF = 1,752**, AIS=0,248, cf. JCR 2017 (publicat în iunie 2018), **Q2 WoS** cf. JCR 2017

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Some of my most relevant publications in:

- **developing (Neural Branch) Prediction Methods**

Below you can have a look on my most important research papers related to these topics (**all the papers are available online through the corresponding links**).

- VINȚAN L. – *Towards a High Performance Neural Branch Predictor*, *Proceedings of The International Joint Conference on Neural Networks - IJCNN '99* (CD-ROM, ISBN 0-7803-



5532-6; Abstract in IJCNN'99 Book of Summaries, art. 2106), pp. 868 – 873, vol. 2, Washington DC, USA, IEEE, 10-16 July 1999. Digital Object Identifier: 10.1109/IJCNN.1999.831066. V. <http://webspace.ulbsibiu.ro/lucian.vintan/html/USA.pdf>

- VINȚAN L., EGAN C. – *Extending Correlation in Branch Prediction Schemes*, Proceedings of the 25<sup>th</sup> Euromicro International Conference, Milano, Italy, 8-10 September, IEEE Computer Society Press, ISBN 0-7695-0321-7, 1999, v. <http://webspace.ulbsibiu.ro/lucian.vintan/html/Milano.pdf>

- VINȚAN L. – *Towards a Powerful Dynamic Branch Predictor*, Romanian Journal of Information Science and Technology (ROMJIST), vol. 3, nr. 3, pg. 287-301, ISSN: 1453-8245, Romanian Academy, Bucharest, 2000, v. [http://webspace.ulbsibiu.ro/lucian.vintan/html/Rom\\_JIST.pdf](http://webspace.ulbsibiu.ro/lucian.vintan/html/Rom_JIST.pdf)

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- FLOREA A., VINȚAN L., MIHU I.Z. – *Understanding and Predicting Indirect Branch Behavior*, Studies in Informatics and Control, Vol. 13, No. 1, pg. 61-82, ISSN: 1220-1766, National Institute for Research and Development in Informatics, Bucharest, March 2004 (indexata IEE INSPEC, cotată ISI Thomson Journals); IF=0,605 in 2013 v. [http://sic.ici.ro/?page\\_id=13](http://sic.ici.ro/?page_id=13), v.

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- L. VINȚAN, A. GELLERT, A. FLOREA, M. OANCEA, C. EGAN – *Understanding Prediction Limits through Unbiased Branches*, Eleventh Asia-Pacific Computer Systems Architecture Conference, Shanghai 6-8<sup>th</sup>, September, 2006, v. <http://webspace.ulbsibiu.ro/lucian.vintan/html/LNCS.pdf>

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- VINȚAN L. N., FLOREA A., GELLERT A. - *Random Degrees of Unbiased Branches*, Proceedings of The Romanian Academy, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 9, Number 3, pp. 259 - 268, ISSN 1454-9069, Bucharest, 2008 - <http://www.academiaromana.ro/sectii2002/proceedings/doc2008-3/13-Vintan.pdf>

- VINȚAN L. – *Neural Branch Prediction: from the First Ideas, to Implementations in Advanced Microprocessors and Medical Applications*, Proceedings of The Romanian Academy, Series A: Mathematics, Physics, Technical Sciences, Information Science, Volume 20, Number 2, pp. , ISSN 1454-9069, Bucharest, 2019. Revista este cotată Clarivate Analytics Web of Science, IF = 1,752, AIS=0,248, cf. JCR 2017 (publicat în iunie 2018), Q2 WoS cf. JCR 2017 (25/64 Multidisciplinary Sciences (Information Science)),

Some of my most relevant publications in:

- developing New Computer Architectures (C. A.) and Research Methods in C. A.

Below you can have a look on my most important research papers related to these topics (all the papers are available online through the corresponding links).

- STEVEN G., VINȚAN L. – *Modelling Superscalar Pipelines with Finite State Machines*, "Proceedings of the 22<sup>nd</sup> Euromicro'96 Conference. Beyond 2000:

Hardware/Software Design Strategies", September 1996, Prague, Czech Republic, pp. 20-25, IEEE Computer Society Press, Los Alamitos, California, USA, ISBN 0-8186-7703-1, Library of Congress Number 96-79894, v.  
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- VINTAN L., STEVEN G.B. – *Memory Hierarchy Limitations in Multiple Instruction-Issue Processor Design*, "Proceedings of the 23-rd Euromicro Conference. New Frontiers of Information Technology", September 1997, Budapest, Hungary, pp. 252-257, IEEE Computer Society Press, Los Alamitos, California, USA, 1997, ISBN 0-8186-8215-9, Library of Congress Number 97-81043, v.  
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- STEVEN F. L., POTTER R., STEVEN G. B., VINTAN L. – *Static Data Dependence Collapsing in a High Performance Superscalar Architecture*, The 3-rd International Conference on Massively Parallel Computing Systems (MPCS '98), Colorado Springs, USA., 6-9 April, 1998, v.  
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- VINTAN L., ARMAT C., STEVEN G. – *The Impact of Cache Organisation on the Instruction Issue Rate of a Superscalar Processor*, **Proceedings of Euromicro 7-th Workshop on Parallel and Distributed Systems**, pp. 58-65, ISBN 0-7695-0059-5, Funchal, Portugal, 3<sup>rd</sup> – 5<sup>th</sup> February, IEEE Computer Society Press, 1999, v.  
<http://webpace.ulbsibiu.ro/lucian.vintan/html/Euro1999.pdf>
- STEVEN G., EGAN C., SHIM W., VINTAN L. – *Applying Caching to Two-Level Adaptive Branch Prediction*, Proceedings of the **International Euromicro Conference DSD '2001**, IEEE Computer Society Press, ISBN 0-7695-1239-9, Warsaw, Poland, September, 2001 (pg.186-193), v.  
<http://webpace.ulbsibiu.ro/lucian.vintan/html/CACHING1.PDF>

Some of my most relevant publications in:

- developing **Prediction Methods with IT Applications**

Below you can have a look on my most important research papers related to these topics (**all the papers are available online through the corresponding links**).

- PETZOLD J., BAGCI F., TRUMLER W., UNGERER T., VINTAN L. – *Global State Context Prediction Techniques Applied to a Smart Office Building*, 2004 **Communication Networks and Distributed Systems Modelling and Simulation Conference (CNDS '04)**, San Diego, California, USA, January 18-21, 2004, v.  
[http://webpace.ulbsibiu.ro/lucian.vintan/html/Prediction\\_CNDS.pdf](http://webpace.ulbsibiu.ro/lucian.vintan/html/Prediction_CNDS.pdf)
- VINTAN L., GELLERT A., UNGERER T., PETZOLD J. – *Person Movement Prediction Using Neural Networks*, KI 2004 Proceedings Workshop on Modelling and Retrieval of Context, University of Ulm, Germany, ISSN 1613-0073, September 20<sup>th</sup> - 21<sup>st</sup> 2004, v.  
[https://www.researchgate.net/profile/Lucian\\_Vintan/publication/228769110\\_Global\\_state\\_context\\_prediction\\_techniques\\_applied\\_to\\_a\\_smart\\_office\\_building/links/53e9d8a30cf2dc24b3cadce9/Global-state-context-prediction-techniques-applied-to-a-smart-office-building.pdf](https://www.researchgate.net/profile/Lucian_Vintan/publication/228769110_Global_state_context_prediction_techniques_applied_to_a_smart_office_building/links/53e9d8a30cf2dc24b3cadce9/Global-state-context-prediction-techniques-applied-to-a-smart-office-building.pdf),  
[http://webpace.ulbsibiu.ro/lucian.vintan/html/NN\\_Context.pdf](http://webpace.ulbsibiu.ro/lucian.vintan/html/NN_Context.pdf)
- GELLERT A., VINTAN L. – *Person Movement Prediction Using Hidden Markov Models*, Studies in Informatics and Control, Vol. 15, No. 1, pp. 17-30, ISSN: 1220-1766, National Institute for Research and Development in Informatics, Bucharest, March 2006

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<http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.322.2127&rep=rep1&type=pdf>

Some of my most relevant publications in:

- developing **Text Mining Research Methods**

Below you can have a look on my most important research papers related to these topics (**all the papers are available online through the corresponding links**).

- D. MORARIU, L. VINȚAN, V. TRESP – *Feature Selection Methods for an Improved SVM Classifier*, Enformatika Journal, Transactions on Engineering, Computing and Technology, vol. 14, August 2006, pp. 83-89, ISBN/ISSN 1305-5313, (World Enformatika Conference, 3<sup>rd</sup> International Conference on Intelligent Systems, ICIS 2006), Prague, Czech Republik, 2006, v.  
<http://acaps.ulbsibiu.ro/papers/Morariu Prag Enf.pdf>
- D. MORARIU, L. VINȚAN, V. TRESP – *Evolutionary Feature Selection for Text Document using SVM*, Proceedings of XV International Conference on Computer and Information Science and Engineering, pp. 215-221, October 22-24, 2006, Barcelona, Spain, CISE 2006, ISBN: 975-00803-4-3, v.  
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- D. MORARIU, L. VINȚAN, V. TRESP – *Meta-classification using SVM classifiers for Text Document*, Proceedings of XV International Conference on Computer and Information Science and Engineering, pp. 222-227, October 22-24, 2006, Barcelona, Spain, CISE 2006, ISBN: 975-00803-4-3, v.  
[https://www.researchgate.net/publication/242580604 Meta-Classification using SVM Classifiers for Text Documents](https://www.researchgate.net/publication/242580604_Meta-Classification_using_SVM_Classifiers_for_Text_Documents)
- D. MORARIU, M. VINȚAN, L. N. VINȚAN – *Aspects concerning SVM Method's Scalability*, Studies in Computational Intelligence (SCI). Advances in Intelligent and Distributed Computing, Volume 78, pp. 125-134, Springer-Verlag Berlin Heidelberg, ISSN 1860-949X, ISBN 978-3-540-74929-5, 2008, v.  
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- MORARIU D., CRETULESCU R., VINȚAN L. – *Improving a SVM Meta-classifier for Text Documents by using Naïve Bayes*, International Journal of Computers, Communications & Control (IJCCC), Agora University Editing House - CCC Publications, ISSN 1841 – 9836, E-ISSN 1841-9844, Vol. 5, No. 3, pp. 351-361, 2010, v.  
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- CRETULESCU R., MORARIU D., VINȚAN L., COMAN I. D. – *An Adaptive Meta-classifier for Text Documents*, The 16<sup>th</sup> International Conference on Information Systems Analysis and Synthesis (ISAS 2010), vol. 2, pp. 372-377, ISBN-13: 978-1-934272-88-6, Orlando Florida, USA, April 6<sup>th</sup> – 9<sup>th</sup> 2010, v.  
[https://www.researchgate.net/publication/264702265 An Adaptive Meta-classifier for Text Documents](https://www.researchgate.net/publication/264702265_An_Adaptive_Meta-classifier_for_Text_Documents)
- R. G. CRETULESCU, D. I. MORARIU, M. BREAZU, L. N. VINȚAN – *Weights Space Exploration using Genetic Algorithms for Meta-classifier in Text Document Classification*, Studies in Informatics and Control, Vol. 21, Issue 2, pp. 147-154, ISSN: 1220-1766, National Institute for Research and Development in Informatics (ICI), Bucharest, 2012, v. [https://sic.ici.ro/wp-content/uploads/2012/06/SIC\\_2012-2-Art4.pdf](https://sic.ici.ro/wp-content/uploads/2012/06/SIC_2012-2-Art4.pdf)

- MORARIU D., CRETULESCU R., VINȚAN L. – *Vector versus Tree Model Representation in Document Clustering*, Romanian Journal of Information Science and Technology (ROMJIST), vol. 16, no. 1, pp. 81-102, ISSN: 1453-8245, **Romanian Academy**, Bucharest, 2013. Disponibil online la adresa: [http://www.imt.ro/romjist/Volum16/Number16\\_1/pdf/06-LVințan.pdf](http://www.imt.ro/romjist/Volum16/Number16_1/pdf/06-LVințan.pdf)
- VINȚAN L., MORARIU D., CREȚULESCU R., VINȚAN M. – *An Extension of the VSM Documents Representation*, International Journal of Computers, Communications & Control, ISSN 1841–9836, Vol. 12, Issue 3, pp. 403 - 414, June 2017, v. <http://univagora.ro/jour/index.php/ijccc/article/view/2889/pdf>
- C. BÂNCIOIU, M. VINȚAN, L. VINȚAN – *Efficiency Optimizations for Koller and Sahami's Feature Selection Algorithm*, Romanian Journal of Information Science and Technology (ROMJIST), Vol. 22, No. 1, pp. 85-99, ISSN: 1453-8245, **Romanian Academy**, Bucharest, 2019

### Some comments related to his scientific work

- [Dr. Daniel Jimenez \(Rutgers University, USA\)](#) wrote in one of his scientific papers (**MICRO-36 Conference, 2003**): "Dynamic branch prediction with neural methods was first proposed by **Vințan**."
- [Dr. Alan Fern et al. \(Purdue University, USA\)](#) wrote in another scientific paper (**Journal of Systems Architecture, Elsevier, 2006**): "[...] prior to our original Dynamic Decision Tree Predictor there was only one such proposal [**Vințan**]."
- [Dr. David Tarjan & Dr. Kevin Skadron \(Virginia University, USA\)](#) wrote in their paper (**ACM Transactions on Architecture and Code Optimization , 2005**): " The idea of the neural branch predictor was originally introduced by **Vințan**."
- [Dr. M. Monchiero & G. Palermo \(Politecnica di Milano\)](#): "Branch predictors based on neural methods have been recently studied [**Vințan** 99], [Jimenez 02,03], showing that they are the most accurate predictor in the literature.""(**LNCS, 2005**)
- [Dr. M. Aamer et al \(University of Pennsylvania, TR 2005\)](#): "In this paper we detail and implement the pre-computed branch prediction algorithm described in [**Vințan** et al]."
- [Prof. dr. hab. Theo Ungerer \(University of Augsburg, Germany, Report, 2002 & Preface to my book, 2007\)](#): "**Prof. Vințan** of University "Lucian Blaga" in Sibiu, Romania, paved the way for neural network predictors by his 1999 paper on the use of neural networks in dynamic branch prediction and is one of the leading scientists in this domain. **Prof. Vințan** is one of the rare European scientist working in the research field of prediction techniques in computer architecture [...] his neural branch predictor is well-known in the research community."
- [Prof. dr. Gordon Steven \(University of Hertfordshire, UK, Report, 2002\)](#): "I have always been extremely impressed by Prof. **Vințan**'s novel research ideas and by the very large amount of research he manages to successfully undertake."
- "I am pleased to confirm the title of Visiting Research Fellow. The title is intended to recognise your ongoing collaboration with our research activities" - [Prof. J. M. Senior, Dean of the Faculty of Engineering and Information Sciences, University of Hertfordshire, UK \(April 2003\)](#)

- **Authors from (INTEL CO, USA, Symposium on HPCA, 2004):** „Perceptrons have been proposed earlier for branch prediction [Vințan]...”
- **Dr. Colin Egan (University of Hertfordshire, UK, in Journal of Systems Architecture, 2003, Elsevier):** "The first known perceptron branch predictor was developed by Vințan."
- **Culpepper B., Gondree M. (University of California, UC Davis, USA):** "There is a growing trend among researchers to apply machine learning techniques to the problem of branch prediction, starting with Vințan ." (Techn. Rep. 2005)
- **Amilcar Arfel Molina D'iaz (University Politecnica Catalunya, TR 2005, Barcelona):** "La idea de este perceptron fue introducida originalmente por Vințan." (Cited Paper, 2005; see above)
- **Dr. Jan Petzold (University of Augsburg, Germany, PhD Thesis, 2005):** "Prof. dr. Lucian Vințan danke ich fur die Zusammen arbeit und die vielen hilfreichen Diskussionen wahrend und nach seines Aufenthaltes in Augsburg."
- **Dr. Veerle Desmet (Gent University, Belgium, PhD Thesis, 2006):** "Vințan first proposed dynamic branch prediction based on neural networks."
- **J. Singer et al. (Workshop on Statistical and Machine Learning Approaches Applied to Architectures and Compilation, 2007):** "Vințan pioneers the idea of using perceptrons for branch prediction"
- **Professors J. Silc, T. Ungerer, B. Robic (International Journal on High Performance Systems Architecture, vol. 1, no. 1, 2007):** "The first dynamic neural branch predictors were proposed by Vințan"
- **Dr. M. Black ( Applying Perceptrons to Speculation in Computer Architecture-Neural Networks in Future Microprocessors, Vdm Verlag, 2007):** " It is important to note that this (my note: referring to Jimenez's paper published in 2000) is not the very first neural branch predictor proposed. Two neural approaches were proposed in a paper by Vințan in 1999."
- **Dr. Pan et al (Northwestern Polytechnical University, Xi'an, China, ACSAC 2007):** "These years, some new methods are introduced such as Lucian N. Vințan's pre-computed branches"
- **Dr. A. Sez nec (Journal of ILP 9, USA, 2007):** "The introduction of the neural based branch predictors [Vințan, Jimenez] provided a solution for effectively combining several predictions."
- **O. Kirby (TR, Canada, 2007):** "The earliest proposal for using neural networks to predict branches we are aware of was by Vințan."
- **V. Uzelac (MSc Thesis, USA, 2009):** "Neural branch prediction is first proposed by Vințan. Vințan considers branch prediction as a particular problem in a broader class of pattern recognition problems that can be solved by neural networks."
- **Dr. S. Verma (PhD Thesis, Louisiana State University, USA, December 2011):** „The idea of neural branch prediction was originally introduced by Vințan”
- **Dr. D. Jimenez (University of Texas at San Antonio, USA: An Optimized Scaled Neural Branch Predictor, Paper IEEE ICCD 2011 Conference):** “The SNP/SNAP (Scaled Neural Analog Predictor) predictor is based on neural branch prediction, a technique introduced by Vințan [21] and refined by Jimenez et al. [8].”



- **Dr. Erich Bruns, Prof. Oliver Bimber** (IEEE Pervasive Computing, pp. 74-81, April-June 2012): “**Lucian Vințan** and his colleagues introduced an approach for predicting person movement by applying neural networks.”
- **Professor Emeritus Lotfi A. Zadeh** (University of California, Berkeley, USA, September 23-rd 2012, personal e-mail message): “Your work [**Vințan**] appears to be very interesting.”
- **Wikipedia**: “Machine learning for branch prediction using LVQ and multi-layer perceptrons, called "neural branch prediction", was proposed by **Prof. Lucian Vințan** (Lucian Blaga University of Sibiu).”
- The **Meltdown** paper authors – this famous paper (2018) is available online at <https://meltdownattack.com/meltdown.pdf> – cited my IJCNN 1999 paper that introduces the fertile idea of neural branch prediction. They wrote that “*More recently, ideas to use neural branch prediction [34, (– my paper!) 18, 32] have been picked up and integrated into CPU architectures [3].*” In their also famous *Spectre* paper (2018), available online at <https://spectreattack.com/spectre.pdf>, the authors wrote that neural branch predictors involve „*even more complex speculative behavior.*” It is well-known that the authors of these two papers and, independently, some Google researchers (see <https://googleprojectzero.blogspot.ro/2018/01/reading-privileged-memory-with-side.html>) have discovered some commercial processors bugs related to the subtle complex interaction between out of order and speculative instructions execution, caches and virtual memory protection mechanism.
- **Dr. Michaud P.** (ACM Transactions on Architecture and Code Optimizations, 2018) „In 1999-2000, two research teams, independently, started exploring the use of artificial neural networks for branch prediction [**Vințan**, Jimenez].”

#### Distinctions, Awards & Honours:

- Elected Full-Member (see <http://www.astr.ro/prof-univ-dr-ing-lucian-nicolae-vintan/>) of the "**Technical Sciences Academy of Romania**" ("**Academia de Stiinte Tehnice din Romania - ASTR**", **IT&C Section**, July 2012; Elected **Correspondant Member** in April 2005, President **Acad. Radu Voinea**. During 2005-2012 I was the youngest Academy's Member.) See here **DIPLOMA - ASTR**.
- "**Tudor Tanasescu**" **Romanian Academy Prize in 2005**, for the book entitled "**Microarchitectures Simulation and Optimization**" (in Romanian), Matrix Rom Publishing House, Bucharest, 2003 (co-author: Dr. Adrian Florea, 443 pages, CD attached) - see **DIPLOMA - ACADEMIA ROMANA**.
- **Visiting Research Fellow** Title from University of Hertfordshire (UH), UK (2002); "*I am pleased to confirm the title of Visiting Research Fellow. The title is intended to recognise your ongoing collaboration with our research activities.*" - Professor J. M. Senior, PhD, Dean of the Faculty of Engineering and Information Sciences, UH (April 2003)
- Some old **national awards** obtained in 1984 - in **Mathematics** domain - when I was a student in Computer Science & Engineering: **Diploma1**, **Diploma2**, **Diploma3**... and some more recent **LBUS Awards and Recognitions**: **Award1**, **Award2**, **Award3**, **Award4**, **Award5**, etc.
- **Active European Commission Expert** in Computing Systems domain (from 2005 - present)
- **Best Paper Award** at *The IEEE RoEduNet 2010 International Conference* (with my PhD student **Horia Calborean**) - see **Diploma - RoEduNet**

- International Program Committee Member of the prestigious [Branch Prediction Championship - 2011](#) organized by [INTEL Co.](#) and [JILP](#) (in conjunction with [ISCA-38 Conference](#) - the premier forum in Computer Architecture). See here: [Introduction](#) and [Results](#)
- Academy of Technical Sciences of Romania [Jubilee Medal](#) (2012); Academy of Technical Sciences of Romania [Anniversary Diploma](#) and [Anniversary Medal](#) (2017)
- [Diploma of Excellence](#) from Matrix Rom Publishing House Bucharest "*as recognition of your significant contribution in developing Romanian scientific literature*" (2013)
- [“Bologna Professor” Award](#) offered by The National Alliance of Student Organisations in Romania (ANOSR), Bucharest 2014. [Diploma](#)
- [Diploma of Excellence](#) from Politehnica University of Bucharest "*as a valuing sign for the significant contribution in developing Computer Engineering and Information Technology domain.*" (2014); [Diploma](#) from "Transilvania" University of Brasov (2016); Jubilee [Diploma](#) from Politehnica University of Timisoara (2016); Jubilee [Diploma](#) from University of Pitesti (2016)
- CNCSIS / UEFISCDI Awards for some published Web of Science journal papers (B57, B58, B59, B80, B94, B101, B102, B105, etc.)
- [International Doctoral Committee Member](#) (professors from The Netherlands, France, Spain and Romania), Delft University of Technology (*Technische Universiteit Delft*), The Netherlands (November 2018). [Picture 1](#) [Picture 2](#) [Picture 3](#)
-

Details about his professional activity are online available at <http://webpace.ulbsibiu.ro/lucian.vintan/html/>, <https://www.hipeac.net/~lucian.vintan/>, <http://csac.ulbsibiu.ro/cv.php?q=4>, <http://www.astr.ro/prof-univ-dr-ing-lucian-nicolae-vintan/>.

20.09.2018, Sibiu

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<http://www.astr.ro/prof-univ-dr-ing-lucian-nicolae-vintan/> ( RO)