

PERSONAL INFORMATION

Traian Tulbure



 Transilvania University, Brasov, Romania

 traian.tulbure@unitbv.ro

Sex Male | Nationality Romanian

WORK EXPERIENCE

- 2009 - present **Lecturer**
 Transilvania University of Braşov, Romania, Dept. of Electronics & Computers

 - Teaching digital electronic, computer networks, VLSI design

[Higher education](#)
- 2018 - present **Engineering Manager**
 Intel, Programmable Solutions Group, Braşov, Romania

 - Leading a team of engineers to implement customer designs

[Technology, hardware](#)
- 2006 - 2018 **Customer Engineering Manager**
 eASIC Corporation, Braşov, Romania

 - Leading a team of application engineers to implement customer designs in structured ASIC technology

[Technology, hardware](#)
- 2003-2009 **Assistant Lecturer**
 Transilvania University of Braşov, Romania, Dept. of Electronics & Computers

 - Teaching digital electronics labs, JAVA labs, microprocessors labs

[Higher education](#)
- 2000-2006 **System engineer**
 eASIC Corporation, Braşov, Romania

 - Developing and testing IP core, development of EDA software and scripts

[Technology, hardware](#)

EDUCATION AND TRAINING

- 2001-2011 **Doctor of Philosophy in Electronics and Telecommunications**
 Transilvania University, Electrical Engineering and Computer Science Faculty, Braşov, Romania

 - Microelectronic reconfigurable architectures for structured ASIC technology
- 2000-2001 **Master of Science**
 Transilvania University, Electrical Engineering and Computer Science Faculty, Braşov, Romania

 - EDA techniques

1995-2000 **Bachelor Degree**

Transilvania University, Electrical Engineering and Computer Science Faculty, Braşov (Romania)

- Digital Electronics, Hardware modelling languages (Verilog, VHDL), Image Processing, Microprocessors Architecture, Programming Languages and Object Oriented Programming (C, C++, Java), Embedded Systems, Machine Learning Techniques

PERSONAL SKILLS

Mother tongue(s) Romanian

Other language(s)	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C2	C2	C2	C2	C2
German	A1	A1	A1	A1	A1

Levels: A1/A2: Basic user - B1/B2: Independent user - C1/C2 Proficient user
[Common European Framework of Reference for Languages](#)

Communication skills Good communication skills gained through my experience as customer engineering manager.

Organisational / managerial skills ▪ leadership (currently responsible for a team of 6 people)

Job-related skills ▪ good organization and job assignment based on engineers experience.

Digital competence	SELF-ASSESSMENT				
	Information processing	Communication	Content creation	Safety	Problem solving
	Proficient user	Proficient user	Independent user	Proficient user	Proficient user

Levels: Basic user - Independent user - Proficient user
[Digital competences - Self-assessment grid](#)

Driving licence B

ADDITIONAL INFORMATION

- Publications
Presentations
Conferences
- „Testability Optimizations for A Time Multiplexed CPLD Implemented on Structured ASIC Technology” - Traian Tulbure, Radu Găvrus, Romanian Journal of Information Science and Technology, vol.14, no. 4, pp. 392–398, 2011
- „GALS-SA: Synchronous-Asynchronous Configurable Platform” – Răzvan Jipa, Traian Tulbure OPTIM 2010, Braşov, România, Proceedings of Optim Conference, ISBN: 978-1-4244-7019-8, p. 963 – 970
- „High Speed Interface Testchip for Structured ASIC Technology” – Traian Tulbure, Răzvan Jipa, Buletinul Ştiinţific al Universităţii „TRANSILVANIA”, Braşov, România, 2010, ISSN 2065-2119 p. 305 – 312
- „Area Optimized Solution for Structured ASIC Dynamic Reconfigurable PLA”– Traian Tulbure, Annals of the University of Craiova, ICATE 2010, Craiova, ISSN: 1842-4805, p. 245 – 251
- „A Time Multiplexed Programmable Array for Structured ASIC Technology” – Traian Tulbure, ETC 2010, Timişoara, România, Buletinul Ştiinţific al Universităţii „POLITEHNICA” din Timişoara, 2010, ISBN: 978-1-4244-8458-4, p. 25 – 29
- „A Dynamic Reconfigurable CPLD Architecture for Structured ASIC Technology” – Traian Tulbure, ARC 2011, Belfast, United Kingdom, Reconfigurable Computing: Architecture, Tools and Applications, LNCS 6578, ISBN: 978-3-642-19474-0, p. 296 – 301
- „Implementation of an Embedded FPGA Architecture on Structured ASIC” – Traian Tulbure, Proceedings of 4th Edition of International Conference on Electronics, Computers and Artificial Intelligence, ECAI 2011, Piteşti, România, ISSN: 1843-2155, p. 1– 6
- „A Test Optimized Dynamic Reconfigurable CPLD for Structured ASIC Technology”, – Traian Tulbure, Proceedings of International Semiconductor Conference, CAS 2011, Sinaia, România, ISBN:978-1-61284-171-7, p. 347-352
- „A new methodology for dynamic reconfiguration of structured ASIC” – Traian Tulbure, IEEE 18th International Symposium for Design and Technology in Electronic Packaging (SIITME), Alba Iulia, România, ISBN: 978-1-4673-4757-0, p. 299-302
- „The Read Out Controller for the ATLAS New Small Wheel” - R.M. Coliban, S. Popa, T. Tulbure, D. Nicula, M. Ivanovici, S.Martoiu,L. Levinson, J. Vermeulen. Journal of Instrumentation, 11(02):C02069, 2016