

Listă de lucrări

Articole publicate, în afara celor ca autor ATLAS (pentru acestea a se vedea fișa de verificare a îndeplinirii standardelor minime naționale și celor proprii universității):

1. Articole de Jurnal (ISI WoS)

1. **Ș. Popa**, S. Mărtoiu, M. Ivanovici, "Study of the ATLAS new small wheel read-out controller ASIC in a neutron irradiation environment", Journal of Instrumentation (IF = 1.454), Volume 15, October 2020, <https://doi.org/10.1088/1748-0221/15/10/P10023>
2. **Ș. Popa**, S. Mărtoiu, M. Ivanovici, "The quality-control test of the digital logic for the ATLAS new small wheel read-out controller ASIC", Journal of Instrumentation (IF = 1.454), Volume 15, April 2020, <https://doi.org/10.1088/1748-0221/15/04/P04023>
3. R. Coliban, **Ș. Popa**, T. Tulbure, D. Nicula, M. Ivanovici, S. Martoiu, L. Levinson, J. Vermeulen; "The Read Out Controller for the ATLAS New Small Wheel"; Journal of Instrumentation (IF = 1.454); Volume 11, Feb 2016; <https://doi.org/10.1088/1748-0221/11/02/C02069>

2. Articole de conferință

1. **Ș. Popa**, M. Ivanovici, R.-M. Coliban, "Time-multiplexed 10Gbps Ethernet-based Integrated Logic Analyzer for FPGAs", International Symposium on Electronics and Telecommunications (ISETC), Timisoara, 5-6 November 2020, https://conference.etc.upt.ro/isetc2020/openconf/modules/request.php?module=oc_program&action=program.php&p=program
2. **Ș. Popa**, M. Luchian, M. Ivanovici; "Clock and data signals synchronization for an FPGA- based ASIC testing setup"; ISSCS 2019 - International Symposium on Signals, Circuits and Systems; July 2019; <https://doi.org/10.1109/ISSCS.2019.8801780>
3. **Ș. Popa**, S. Martoiu, M. Luchian, R. Coliban, M. Ivanovici; *The Quality-Assurance Test of the ATLAS New Small Wheel Read-Out Controller ASIC*; Volume 343 - Topical Workshop on Electronics for Particle Physics (TWEPP2018) – Posters; July 2019; DOI: 10.22323/1.343.0081; <http://inspirehep.net/record/1747411>
4. D. Nicula, J. Vermeulen, L. Levinson, M. Ivanovici, R.-M Coliban, S. Martoiu, **Ș. Popa**, T. Tulbure, "Design of the NSW Read Out Controller ASIC", Topical Workshop on Electronics for Particle Physics (TWEPP2015), Lisbon, Portugal, 29 September 2015, <https://indico.cern.ch/event/357738/contributions/848825/>

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Semnătură:

